

|   |                      |                     |            |
|---|----------------------|---------------------|------------|
| <b>INFORMATION DISCLOSURE<br/>STATEMENT BY APPLICANT</b><br>( Not for submission under 37 CFR 1.99) | Application Number   |                     | 10002217   |
|   | Filing Date          |                     | 2001-11-01 |
|   | First Named Inventor | Craig Nemecek       |            |
|   | Art Unit             | 2123                |            |
|   | Examiner Name        | Jason Scott Proctor |            |
| Attorney Docket Number  |                      | CYPR-CD01207M       |            |

| U.S.PATENTS       |         |               |                        |            |   | Remove   |
|-------------------|---------|---------------|------------------------|------------|---|--|
| Examiner Initial* | Cite No | Patent Number | Kind Code <sup>1</sup> | Issue Date | Name of Patentee or Applicant of cited Document | Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear |
|                   | 1       |               |                        |            |   |  |
|                   | 2       |               |                        |            |   |  |

If you wish to add additional U.S. Patent citation information please click the Add button.

Add

| U.S.PATENT APPLICATION PUBLICATIONS |         |                    |                        |                  |   | Remove   |
|-------------------------------------|---------|--------------------|------------------------|------------------|---|--|
| Examiner Initial*                   | Cite No | Publication Number | Kind Code <sup>1</sup> | Publication Date | Name of Patentee or Applicant of cited Document | Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear |
|                                     | 1       |                    |                        |                  |   |  |

If you wish to add additional U.S. Published Application citation information please click the Add button.

Add

| FOREIGN PATENT DOCUMENTS |         |                                      |                             |                        |                  |   | Remove   |                          |
|--------------------------|---------|--------------------------------------|-----------------------------|------------------------|------------------|---|--|--------------------------|
| Examiner Initial*        | Cite No | Foreign Document Number <sup>3</sup> | Country Code <sup>2</sup> j | Kind Code <sup>4</sup> | Publication Date | Name of Patentee or Applicant of cited Document | Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear | T <sup>5</sup>           |
|                          | 1       |                                      |                             |                        |                  |   |  | <input type="checkbox"/> |

If you wish to add additional Foreign Patent Document citation information please click the Add button

Add

| NON-PATENT LITERATURE DOCUMENTS |  |  |  |  |  |  | Remove |
|---------------------------------|--|--|--|--|--|--|--------|
|---------------------------------|--|--|--|--|--|--|--------|

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
( Not for submission under 37 CFR 1.99)

|                        |                     |
|------------------------|---------------------|
| Application Number     | 10002217            |
| Filing Date            | 2001-11-01          |
| First Named Inventor   | Craig Nemecek       |
| Art Unit               | 2123                |
| Examiner Name          | Jason Scott Proctor |
| Attorney Docket Number | CYPR-CD01207M       |

| Examiner Initials* | Cite No | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published. | T5                       |
|--------------------|---------|---|--------------------------|
| /JP/               | 1       | Dahl, et al.; "Emulation of the Sparcle Microprocessor with the MIT Virtual Wires Emulation System"; 1994; IEEE, Pages 14-22  | <input type="checkbox"/> |
| /JP/               | 2       | Bauer et al.; "A Reconfigurable Logic Machine for Fast Event-Driven Simulation"; 1998; Design Automation Conference Proceedings; pages 668-671  | <input type="checkbox"/> |
| /JP/               | 3       | Nam et al.; "Fast Development of Source-Level Debugging System Using Hardware Emulation"; IEEE 2000; pages 401-404  | <input type="checkbox"/> |
| /JP/4              |         | Huang et al.; "Iceberg: An Embedded In-Circuit Emulator Synthesizer For Microcontrollers"; ACM 1999; pages 580-585  | <input type="checkbox"/> |
| /JP/               | 5       | Khan et al.; "FPGA Architectures for Asic Hardware Emulators"; IEEE 1993; pages 336-340   | <input type="checkbox"/> |
| /JP/               | 6       | Oh et al.; "Emulator Environment Based on an FPGA Prototyping Board"; IEEE 21-23; June 2000; pages 72-77  | <input type="checkbox"/> |
| /JP/               | 7       | Hong et al.; "An FPGA-Based Hardware Emulator for Fast Fault Emulation"; IEEE 1997; pages 345-348   | <input type="checkbox"/> |
| /JP/               | 8       | Ching et al.; "An In-Circuit- Emulator for TM320C25"; IEEE 1994; pages 51-56  | <input type="checkbox"/> |
| /JP/               | 9       | Pasternak; "In-Circuit-Emulation in ASIC Architecture Core Designs"; IEEE 1989; pages P6-4.1 P6 4.4   | <input type="checkbox"/> |
| /JP/               | 10      | Melear; "Using Background Modes for Testing, Debugging and Emulation of Microcontrollers"; IEEE 1997; pages 90-97   | <input type="checkbox"/> |

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
( Not for submission under 37 CFR 1.99)

|                        |                     |
|------------------------|---------------------|
| Application Number     | 10002217            |
| Filing Date            | 2001-11-01          |
| First Named Inventor   | Craig Nemecek       |
| Art Unit               | 2123                |
| Examiner Name          | Jason Scott Proctor |
| Attorney Docket Number | CYPR-CD01207M       |

|      |    |  |                          |
|------|----|--|--------------------------|
| /JP/ | 11 | Walters; "Computer-Aided Prototyping For ASIC-Based Systems"; 1991; IEEE Design and Test of Computers  | <input type="checkbox"/> |
| /JP/ | 12 | Anonymous; "JEEN JTAG Embedded Ice Ethernet Interface"; August 1999; Embedded Performance, Inc.; 3 pages   | <input type="checkbox"/> |
| /JP/ | 13 | Sedory; "A Guide to Debug"; 2004; retrieved on May 20, 2005  | <input type="checkbox"/> |
| /JP/ | 14 | "Microsoft Files Summary Judgment Motions"; February 1999; Microsoft PressPass; retrieved on May 20, 2005 from <a href="http://www.microsoft.com/presspass/press/1999/feb99/Feb99/Calderapr.asp">http://www.microsoft.com/presspass/press/1999/feb99/Feb99/Calderapr.asp</a> ; pages 1-6 | <input type="checkbox"/> |
| /JP/ | 15 | Xerox; "Mesa Debugger Documentation"; April 1979; Xerox Systems Development Department; Version 5.0; pages 1-30  | <input type="checkbox"/> |
| /JP/ | 16 | Stallman et al.; "Debugging with the GNU Source-Level Debugger"; January 1994; retrieved on May 2, 2005 from <a href="http://www.cs.utah.edu">http://www.cs.utah.edu</a>   | <input type="checkbox"/> |
| /JP/ | 17 | Wikipedia.org; "Von Neumann architecture"; retrieved from <a href="http://en.wikipedia.org/wiki/Von_Neumann_architecture">http://en.wikipedia.org/wiki/Von_Neumann_architecture</a> on January 22, 2007; pages 1-5   | <input type="checkbox"/> |
| /JP/ | 18 | Stan Augarten; "The Chip Collection-Introduction-Smithsonian Institute"; "state of the Art"; <a href="http://smithsonianchips.si.edu/augarten/p24.htm">http://smithsonianchips.si.edu/augarten/p24.htm</a> 2007  | <input type="checkbox"/> |
| /JP/ | 19 | "POD-Piece of Data, Plain Old Documentation, Plain Old Dos..."; retrieved on November 14, 2005 from <a href="http://www.auditmypc.com/acronym/POD.asp">http://www.auditmypc.com/acronym/POD.asp</a>  | <input type="checkbox"/> |
| /JP/ | 20 | "POD-Wikipedia, the free encyclopedia"; retrieved on November 14, 2005 from <a href="http://en.wikipedia.org/wiki/POD">http://en.wikipedia.org/wiki/POD</a>  | <input type="checkbox"/> |
| /JP/ | 21 | "pod-definition by diet.die.net"; retrieved on November 14, 2005 from <a href="http://diet.die.net/pod">http://diet.die.net/pod</a>  | <input type="checkbox"/> |

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
( Not for submission under 37 CFR 1.99)

|                        |                     |
|------------------------|---------------------|
| Application Number     | 10002217            |
| Filing Date            | 2001-11-01          |
| First Named Inventor   | Craig Nemecek       |
| Art Unit               | 2123                |
| Examiner Name          | Jason Scott Proctor |
| Attorney Docket Number | CYPR-CD01207M       |

|      |    |  |                          |
|------|----|--|--------------------------|
| /JP/ | 22 | "In-Circuit Emulators- descriptions of the major ICEs around"; retrieved on November 14, 2005 from <a href="http://www.algonet.se/~stafiann/developer/emulator.htm">http://www.algonet.se/~stafiann/developer/emulator.htm</a> | <input type="checkbox"/> |
| /JP/ | 23 | CYPR-CD00183; "Capturing Test/Emulation and Enabling Real-Time Debugging Using FPGA for In-Circuit Emulation"; October 10, 2001; 09/975,104; Snyder  | <input type="checkbox"/> |
| /JP/ | 24 | CYPR-CD00182; "In-System Chip Emulator Architecture"; October 10, 2001; 09/975,115; Snyder et al   | <input type="checkbox"/> |

If you wish to add additional non-patent literature document citation information please click the Add button

**EXAMINER SIGNATURE**

|                    |                 |                 |            |
|--------------------|-----------------|-----------------|------------|
| Examiner Signature | /Jason Proctor/ | Date Considered | 05/28/2008 |
|--------------------|-----------------|-----------------|------------|

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> See Kind Codes of USPTO Patent Documents at [www.USPTO.GOV](http://www.USPTO.GOV) or MPEP 901.04. <sup>2</sup> Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>3</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>4</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>5</sup> Applicant is to place a check mark here if English language translation is attached.

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
( Not for submission under 37 CFR 1.99)

|                        |                     |
|------------------------|---------------------|
| Application Number     | 10002217            |
| Filing Date            | 2001-11-01          |
| First Named Inventor   | Craig Nemecek       |
| Art Unit               | 2123                |
| Examiner Name          | Jason Scott Proctor |
| Attorney Docket Number | CYPR-CD01207M       |

**CERTIFICATION STATEMENT**

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

☐ That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

**OR**

☐ That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(2).

- ☐ See attached certification statement.
- ☒ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
- ☐ None

**SIGNATURE**

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.16. Please see CFR 1.4(d) for the form of the signature.

|            |                    |                     |            |
|------------|--------------------|---------------------|------------|
| Signature  | /Amir A. Tabarrok/ | Date (YYYY-MM-DD)   | 2008-05-27 |
| Name/Print | Amir A. Tabarrok   | Registration Number | 57137      |

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1 hour to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

## Privacy Act Statement

The Privacy Act of 1974 (P.L. 93-579) requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether the Freedom of Information Act requires disclosure of these records.
2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.
3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.
4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).
5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.
6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).
7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.
8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspections or an issued patent.
9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.